**NAME OF THE EVENT :** BYTE FLOW INTEGRATION

**BRANCH :**

EEE

Byte Flow Integration: Merge coding and electrical expertise in a technical challenge. Correct code errors, derive outputs, and design circuits using provided components. PSPICE software simulates circuits for analysis. Rules emphasize time management, no code logic changes, and tech use restrictions. Judging criteria cover code accuracy, output precision, component association, simulation accuracy, and circuit design. This event bridges coding and electrical engineering, enhancing problem-solving and design skills. Cash prizes await winners and runners-up. Register early at [Registration Link] to showcase your coding-circuit fusion!

**Team size:**

3 members

**FACULTY COORDS-**

Dr T S L V Ayya Rao (EEE)-9000400388

P. Upendra Kumar (EEE)-9490452378

**STUDENT COORDS**

1. P. Nirmala (6302437659)
2. S. Sravanthi (9550608607)
3. S. Asritha (9347544758)
4. P.Sridevi(9392437625)

**REG FEE- 1000**

**TOTAL PRICE WORTH - 12000**